

FIG. 1

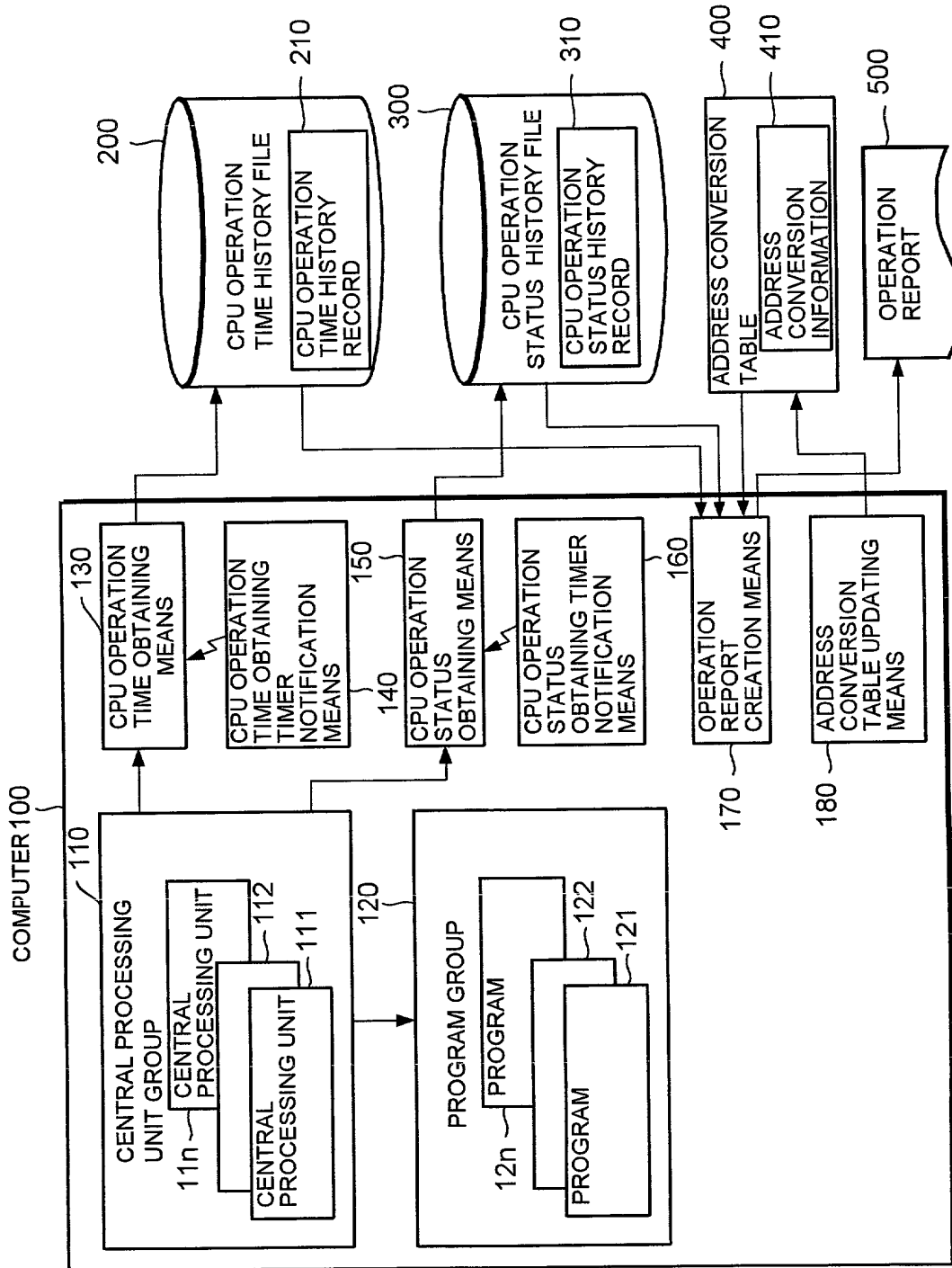


FIG. 2 is a block diagram of a system for monitoring and controlling a process. The system includes a CPU 211, a memory 212, and a display 213. The CPU 211 is connected to the memory 212 and the display 213. The CPU 211 is also connected to a sensor 214, which provides input to the CPU 211. The CPU 211 is further connected to an actuator 215, which provides output to the process. The system is configured to monitor the process and control the actuator based on the input from the sensor.

FIG. 2

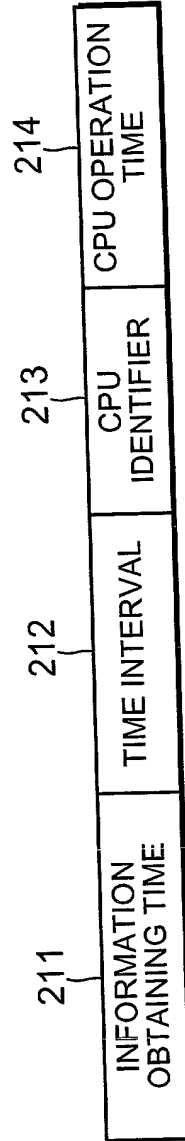


FIG. 3 is a block diagram of a data structure 311. The data structure 311 is divided into four fields: 312, 313, 314, and 315. Field 312 is labeled "TIME INTERVAL", field 313 is labeled "CPU IDENTIFIER", field 314 is labeled "PROGRAM IDENTIFIER", and field 315 is labeled "EXECUTION ADDRESS".

FIG. 3

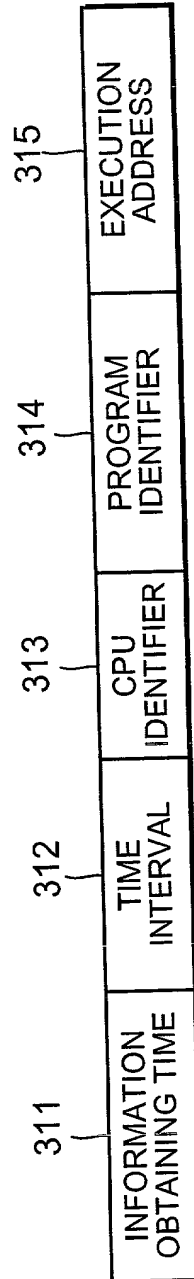


FIG. 4

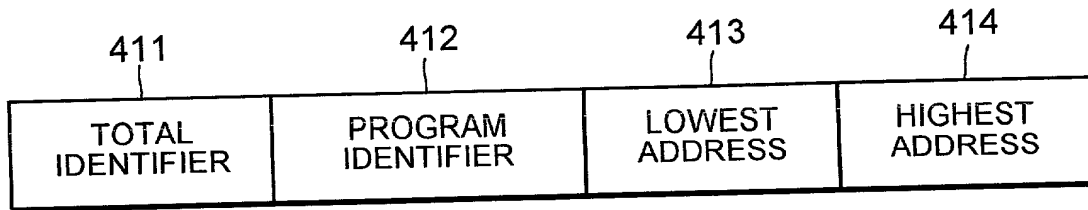


FIG. 5

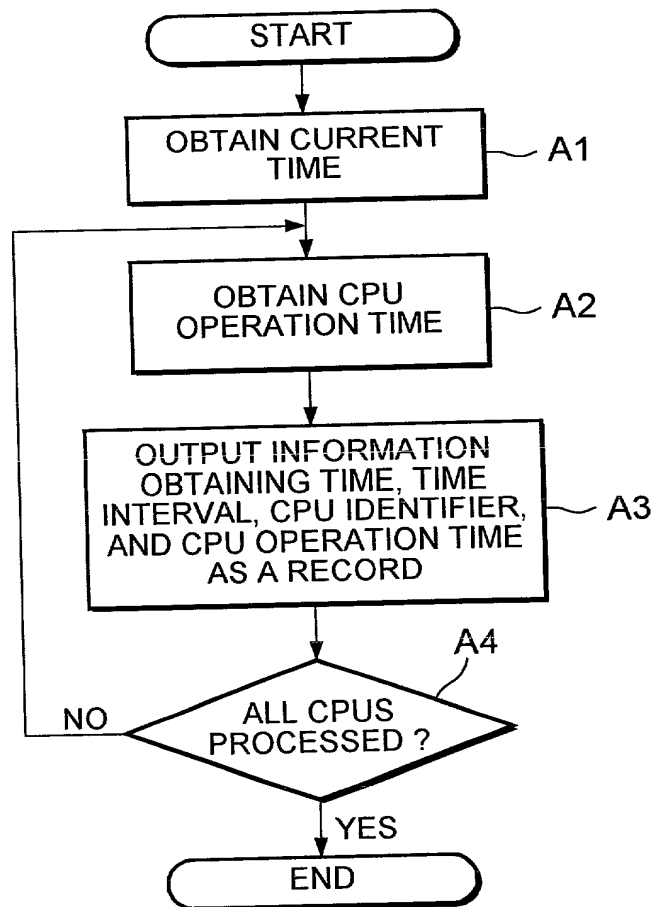


FIG. 6

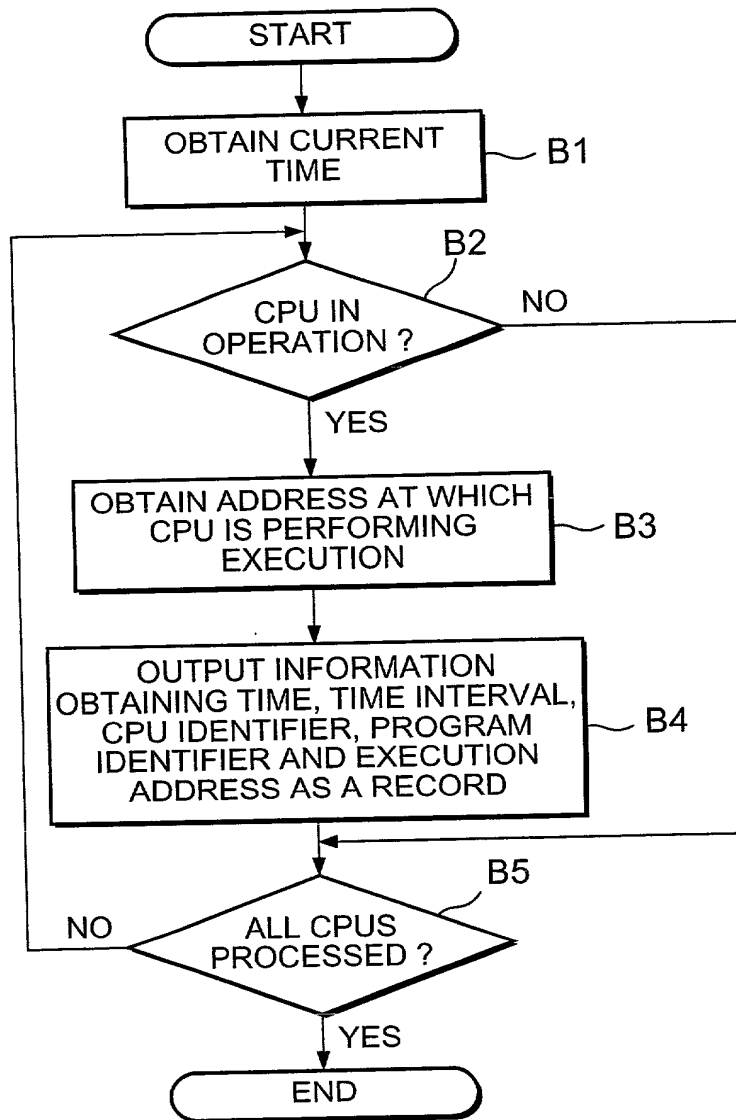


FIG. 7

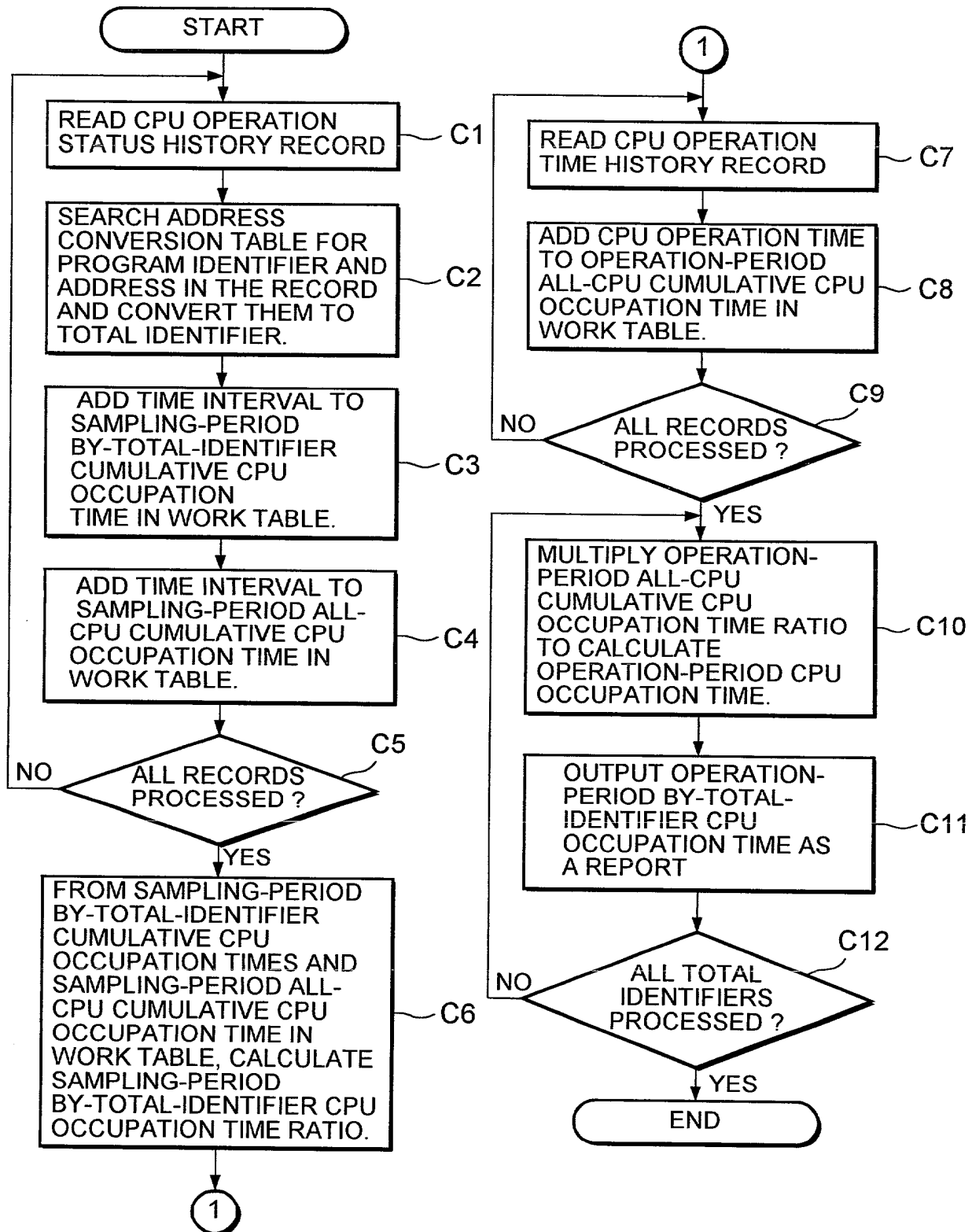


FIG. 8

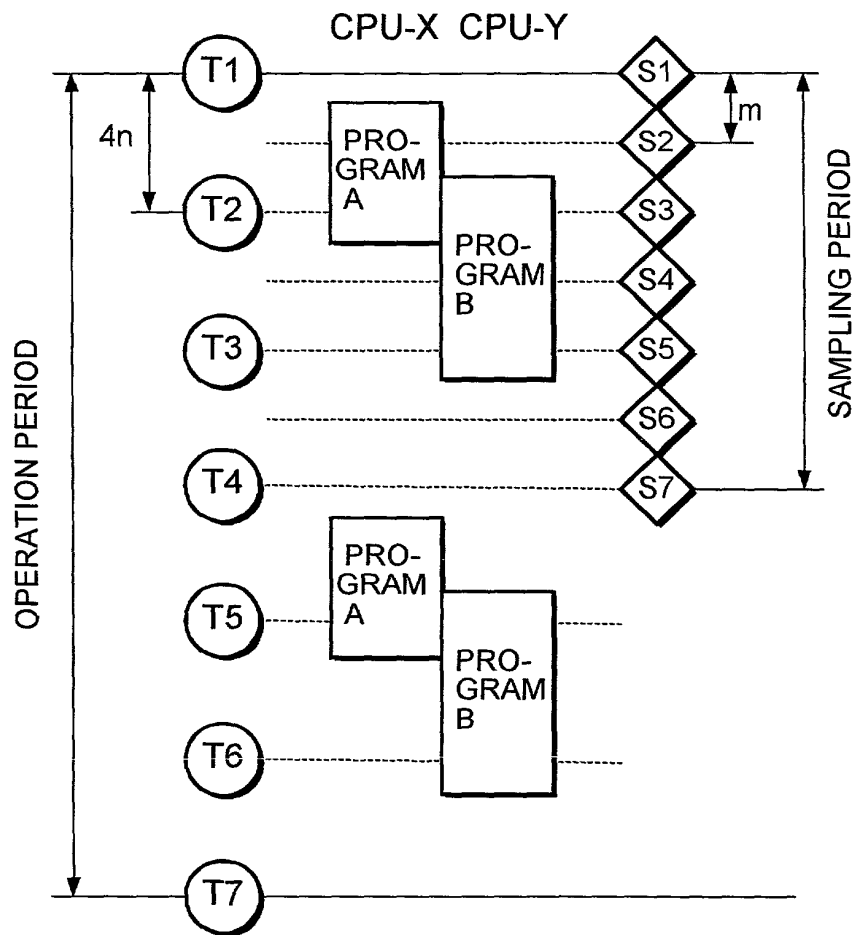


FIG. 9

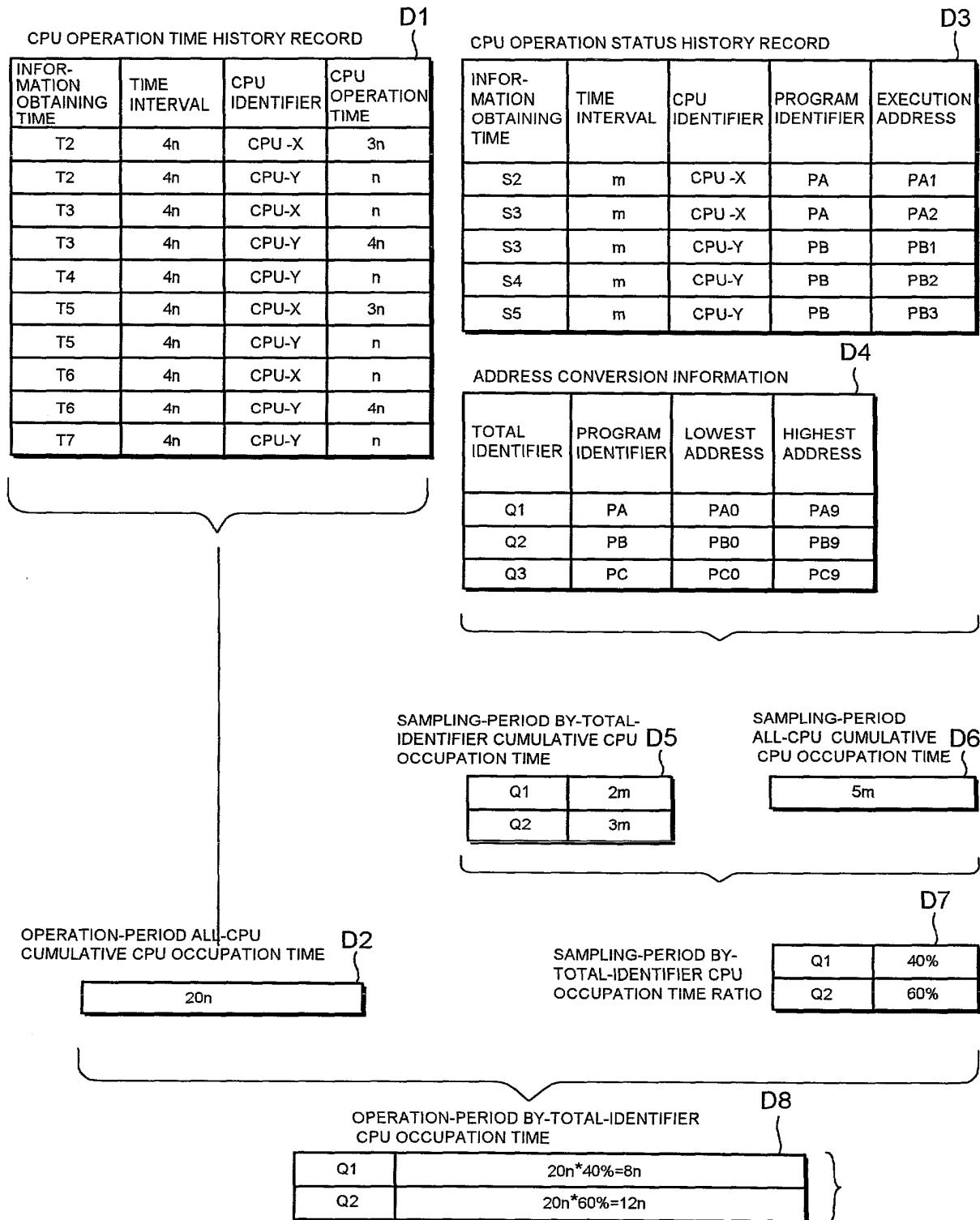




FIG. 10

